

FIG. 1

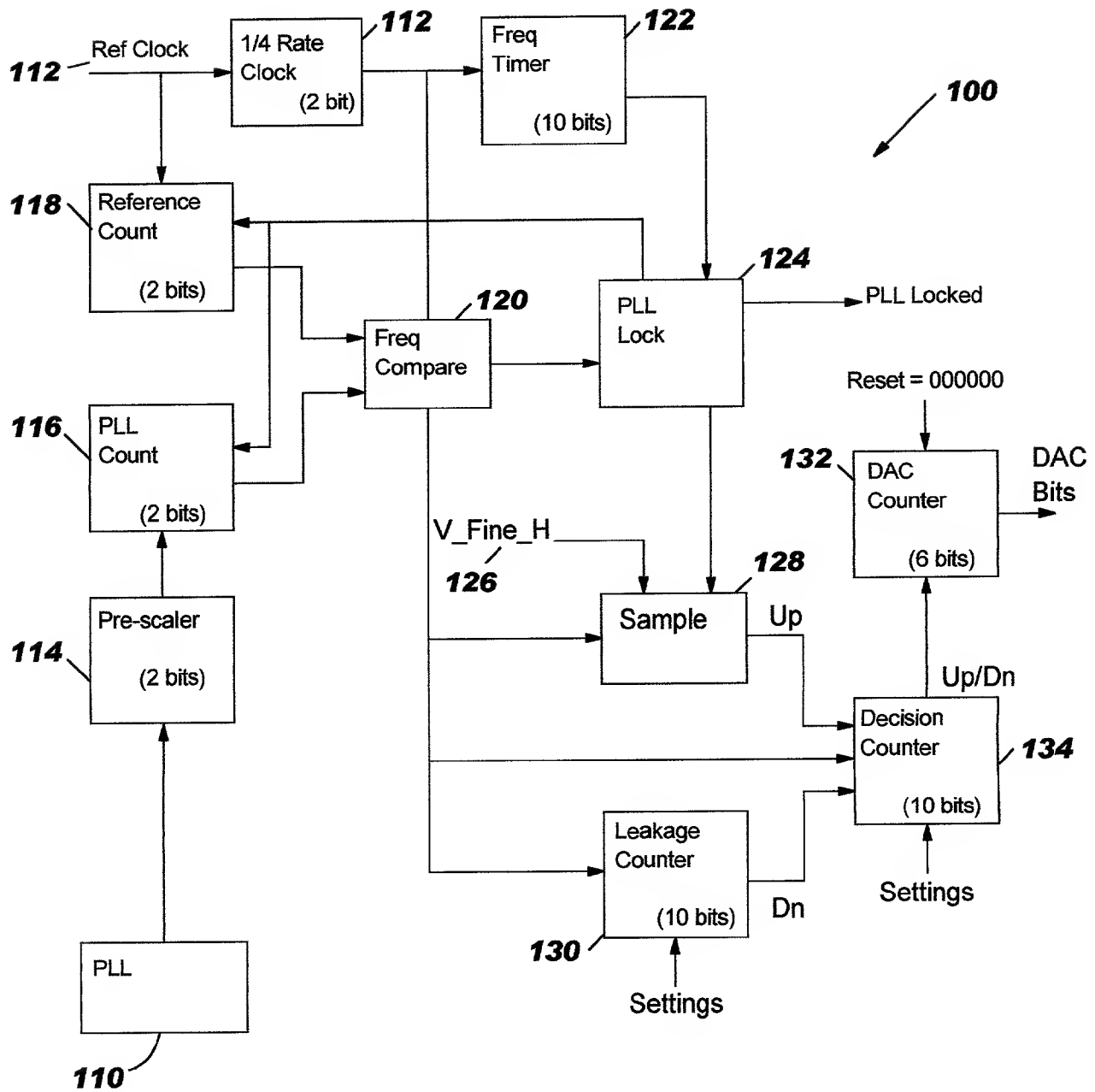
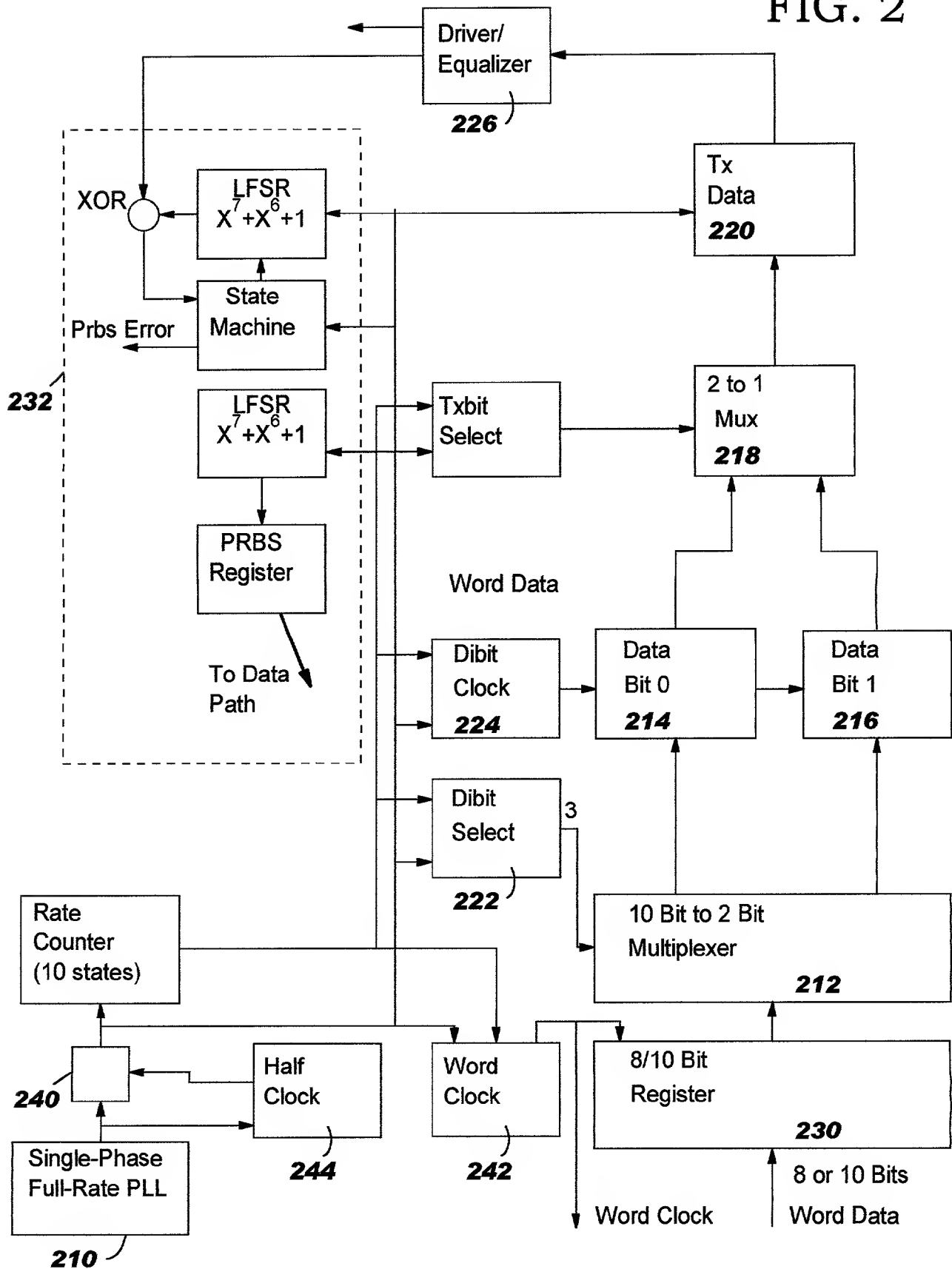


FIG. 2



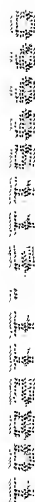
[illegible]

FIG. 4

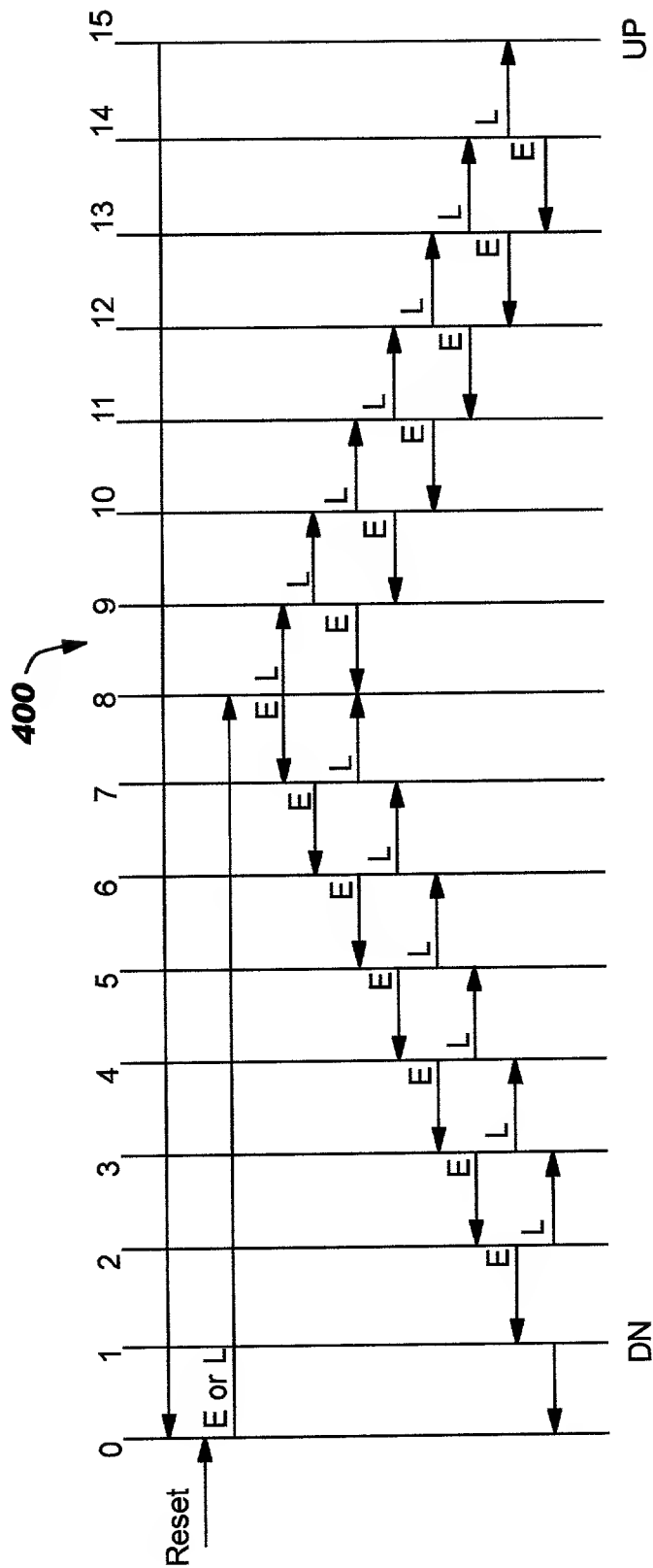
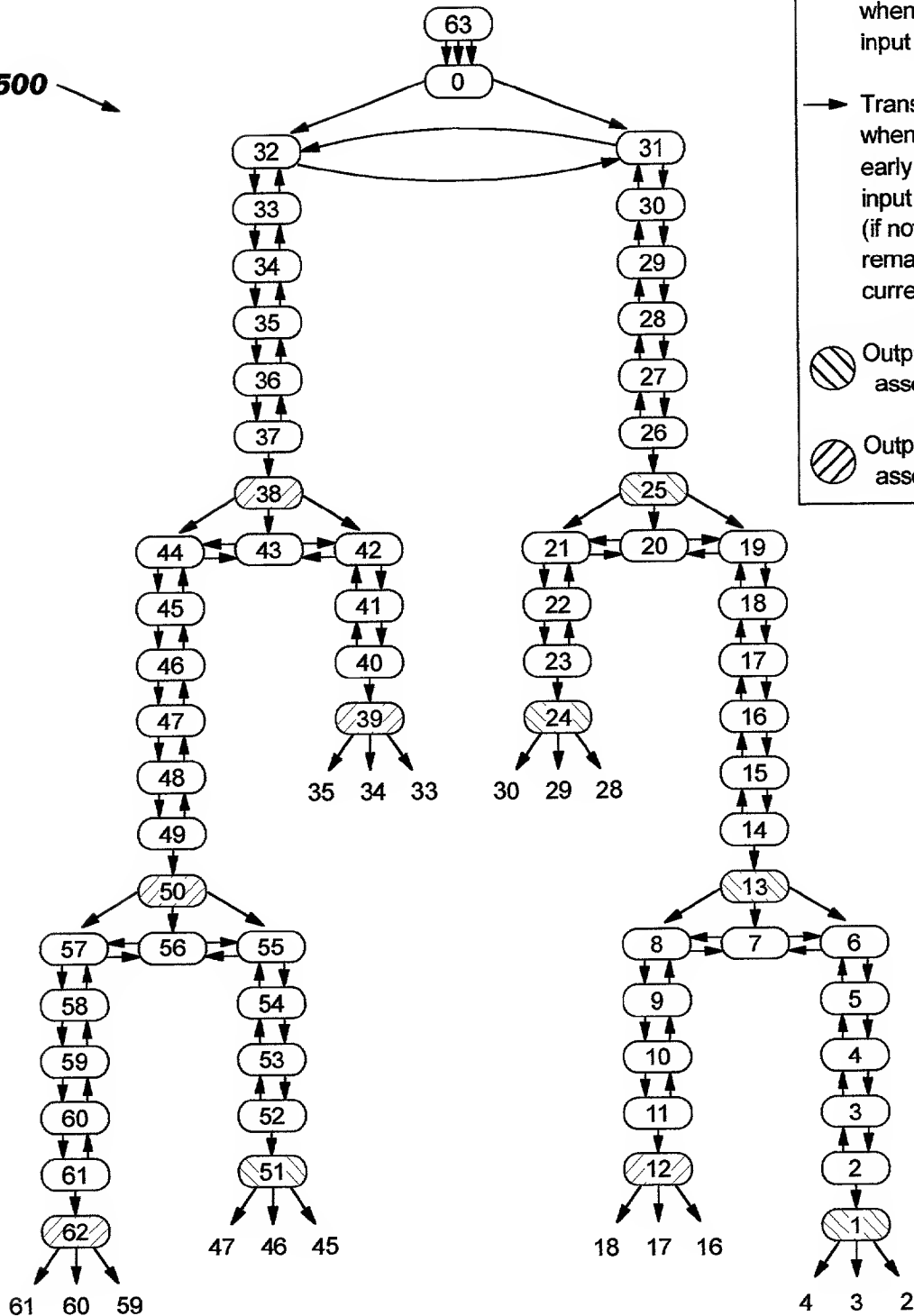


FIG. 5

500



State Diagram Legend

→ Transition taken when late input asserted

→ Transition taken when early input asserted

→ Transition taken when neither early or late input asserted (if not shown, remain in current state)

⬡ Output Down asserted

⬡ Output Up asserted